



## Topographies of integrated circuits

Integrated circuit means a product, in its final form or an intermediate form, in which the elements, at least one of which is an active element, and some or all of the interconnections are integrally formed in and/or on a piece of material and which is intended to perform an electronic function.

[Read more](#) [1]

- [Registration of IC Topographies](#) [2]
- [Application Requirements](#) [3]
- [Legislation](#) [4]
- [Patent attorneys](#) [5]

[Online filling](#) [6]

[Forms](#) [7]

[Fees](#) [8]

**Source URL:** <http://www.agepi.md/en/content/topographies-integrated-circuits>

### Links

[1] <http://www.agepi.md/en/topographies/overview>

[2] <http://www.agepi.md/en/topographies/registration-ic-topographies>

[3] <http://www.agepi.md/en/topographies/application-requirements>

[4] <http://www.agepi.md/en/legislatie/topographies>

[5] <http://www.agepi.md/en/patent-attorneys/topographies>

[6] <https://e-servicii.agepi.gov.md/ro>

[7] <http://www.agepi.md/en/formulare/topographies>

[8] <http://www.agepi.md/en/topographies/fees>